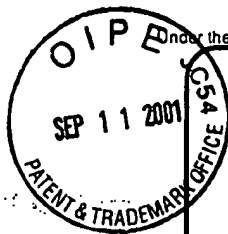


A diagram showing a vertical rod with a horizontal force applied to its left side, pointing to the right. This causes the rod to rotate clockwise.

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Sheet

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Application Number

09/882,087

Filing Date

June 15, 2001

First Named Inventor

Shuo-Yen Robert Li

Group Art Unit

2661

Examiner Name

TRI H. PHAN

Attorney Docket Number

Li 9

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Date
Considered

3/1/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08B (08-00)

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

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Application Number 09/882087
Filing Date 6/15/01
First Named Inventor Shuo-Yen Robert Li
Group Art Unit 2661
Examiner Name TRI H. PHAN
Attorney Docket Number Li 9

2
09/882087
06/15/01

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TP	A	"Access and Alignment of Data in an Array Processor", by Lawrie, IEEE Trans. on Computers, Vol. c-24, No. 12, December, 1975 pgs. 1145-1155	✓
TP	B	"The Knockout Switch: A Simple, Modular Architecture for High-Performance Packet Switching", by Yeh et. al, IEEE Journal on Selected Comm, Vol. SAC-5, No.8, October 1987, pgs. 274-282	✓
TP	C	"Optimal, Multi-Stage Interconnection by Divide-and-Conquer Networks", by Li, Proc. of Second IASTED Conf, December 1998, pgs. 318-323	✓

Examiner
Signature

Date
Considered

3/1/05

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